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# knowlent

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Sandipan Bhanot, CEO of Knowlent Corporation, to Speak at Semico's Semiconductor Intellectual Property Conference on the Topic of Analog IP Portability  
Thursday, June 9, 2005, San Jose, California, Wyndham Hotel

SAN JOSE, CA -- (MARKET WIRE) -- 05/23/2005 --  
[http://www.marketwire.com/mw/release\\_html\\_b1?release\\_id=87362](http://www.marketwire.com/mw/release_html_b1?release_id=87362)

Who

Sandipan Bhanot, CEO of Knowlent Corporation, a supplier of Electrical Verification Platforms (EVPs) and IP that automatically verify the PHY (physical) layer of standard serial and parallel interfaces for electronic designs, will speak on the topic of analog IP portability at Semico's Semiconductor Intellectual Property Conference.

What

Analog IP Panel at Semico's Semiconductor Intellectual Property Conference  
Topic: Is Portable Analog IP Possible, and if so -- When?

When/Where

Thursday, June 9, 2005  
Analog IP Panel: 3:15-4:30pm  
Semico Conference: 8:15 a.m.-4:45 p.m.  
Wyndham Hotel, 1350 North First Street, San Jose, CA 95112

Contacts

For more information about Knowlent's Opal electrical verification platforms, please visit [www.knowlent.com](http://www.knowlent.com).

For more information about the Semico event, please visit <https://www.semico.com/impact.asp>

About Knowlent's Electrical Verification Platforms and IP

Verification platforms and the verification IP market is well established for digital design. Because of the rapid adoption of high speed interface technology, there has been a significant change in interface verification requirements, and thus there is now a need for Electrical Verification Platforms (EVPs) and associated electrical verification IP. Designers now have to thoroughly verify the PHY or electrical layer of the interface because this layer is most vulnerable to failures at higher speeds. Until now, interface verification offerings were limited to the functional level. With Knowlent's Opal Electrical Verification Platform, electrical verification of high speed interfaces is now available to complement functional verification.

About Knowlent Corporation

Knowlent Corporation is a member of ARM's Connected Community, and is a Silicon-Valley-based Electronic Design Automation (EDA) and Intellectual Property (IP) startup providing Electrical Verification Platforms (EVPs) for standards-based high speed serial interface verification.

Knowlent is headquartered at 2255 Martin Avenue, Suite H, Santa Clara, CA 95050 Phone 408 748 0600 Fax 408 748 0605

For more information, please visit Knowlent at [www.knowlent.com](http://www.knowlent.com) or call (408) 748-0600 or email [info@knowlent.com](mailto:info@knowlent.com).

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