

<http://www.prnewswire.com/cgi-bin/stories.pl?ACCT=104&STORY=/www/story/07-18-2006/0004398604&EDATE=>

Altera Announces First AQEC-Compliant FPGAs

Demonstrates Company's Enhanced COTS Mil/Aero Market Strategy

SAN JOSE, Calif., July 18 /PRNewswire-FirstCall/ -- Altera Corporation (Nasdaq: [ALTR](#)) today announced that its Stratix(R) and Cyclone(R) FPGAs have been certified to be in compliance with the Aerospace Qualified Electronic Components (AQEC) specification GEIA-STD-0002-01. The specification, created by more than a dozen semiconductor manufacturers (including Altera) and avionic developers, provides a formal compliance process to assure customers that commercial-off-the-shelf (COTS) components meet the low-risk, high-reliability needs of the military and aerospace electronics market. Altera(R) programmable logic devices are the first to receive AQEC certification.

"Altera is on the right track because they are providing more customer value at a lower cost compared with other players," said Rich Wawrzyniak, senior analyst with Semico Research. "Executing on their enhanced COTS strategy will clearly enable deeper penetration of the military market."

Altera's Enhanced COTS Strategy

Participation in the AQEC working group is a key part of Altera's enhanced COTS strategy, which addresses the unique needs of the military and aerospace market. The strategy includes providing devices that support a wide range of temperature gradients, die business support, long-term obsolescence protection, and a consistent supply chain. Digital signal processing (DSP) performance and density features of the Stratix FPGA series, and the low cost and low power of the Cyclone FPGA series, enable military and aerospace manufacturers to develop highly reliable designs with reduced risk. Altera's Stratix II and Cyclone II FPGAs, and MAX(R) II CPLDs are currently in the process of obtaining AQEC certification. Stratix II FPGAs include anti-tampering design security using a 128-bit advanced encryption standard (AES) design security key.

AQEC Specification

The AQEC specification requires semiconductor manufacturers and distributors to document the processes they use to analyze, test or otherwise characterize their COTS products. The release of the AQEC specification, GEIA-STD-0002-01 "Aerospace Qualified Electric Component Requirements, Volume 1 - Integrated Circuits and Semiconductors," meets a Department of Defense milestone created to enhance the performance and cost benefits of COTS components. The AQEC working group, in conjunction with the Government Electronics and Information Technology Association (GEIA), was responsible for the creation and publication of the specification.

"As a part of the AQEC working group, Altera is at the forefront of redefining the use of COTS in the military and aerospace market," said Don Faria, senior vice president of Altera's business units. "Altera's enhanced COTS strategy reduces risk while providing highly

reliable solutions, which ensure that our customers successfully meet their product commitments."

To learn more about Altera's enhanced COTS strategy, visit <http://www.altera.com/enhanced-COTS>. To read more about the AQEC specification, visit http://66.34.169.103/sstc/G12/agec/AQEC_White_Paper.pdf.

About Altera

Altera's programmable solutions enable system and semiconductor companies to rapidly and cost-effectively innovate, differentiate and win in their markets. Find out more at <http://www.altera.com>.

NOTE: Altera, The Programmable Solutions Company, the stylized Altera logo, specific device designations and all other words that are identified as trademarks and/or service marks are, unless noted otherwise, the trademarks and service marks of Altera Corporation in the U.S. and other countries. All other product or service names are the property of their respective holder.

Editor Contact:

Ford Kanzler

Altera Corporation

408-544-6397

newsroom@altera.com

SOURCE Altera Corporation

Related links:

- <http://www.altera.com/>