



X Initiative Update for April 2005: UMC ready to fabricate 90-nm X Architecture designs  
Semico Research Corporation – X Commentary  
[http://www.xinitiative.com/wt/sec.php?page\\_name=405xnews](http://www.xinitiative.com/wt/sec.php?page_name=405xnews)

In Semico Research Corporation's weekly electronic report, they covered the recent X/UMC 90nm announcement. In addition, Semico wrote an insightful commentary on the benefits of the X Architecture, as well as its future possibilities:

"At Semico, we project that X Architecture designs will become increasingly important and will be employed in more than just high-end ASICs and SOCs as more chip designers realize the speed advantage provided by shorter metal runs and fewer vias as well as the approximate 10% reduction in chip size. Furthermore, since signal delay increases as an exponential function of process node decrease, we believe that X Architecture design technology will be used for many more chip designs at the 65- and 45-nm process nodes." - Dave Cavanaugh, Director of Manufacturing Technology

Jan Willis  
X Initiative Steering Group

-----

These brief newsletters are sent out monthly when there is new information to share from the X Initiative. You can also find a copy of this newsletter and past newsletters on the X Initiative website at <http://www.xinitiative.org> , under "News".