Tabula Launches ABAX(TM) Family of 3-D Programmable Log Devices Delivering Unprecedented Capabilities at Volume Price Points

ABAX Product Family Deployed on TSMC 40 nm Process Leverages Tabula's Spacetime(TM) 3-D Architecture

SANTA CLARA, Calif., March 15 /PRNewswire/ -- Tabula, Inc., a privately held fabless semiconductor company that has developed the Spacetime 3-D architecture, today launched the ABAX family of 3-D Programmable Logic Devices (3PLD). Based on the revolutionary Spacetime architecture that delivers the benefits of 3-D by using time as a third dimension, ABAX enables high-performance, compute-intensive applications on a programmable platform at volume price points. Providing programmability in applications historically served only by ASICs or ASSPs, ABAX represents a new category of programmable logic device. ABAX resets the bar for the capability and density of programmable logic, memory and signa processing while maintaining a familiar design flow. Designed for a wide range of applications, ABAX devices will initially target the telecom, enterprise, and wireless infrastructure markets.

(Photo: http://www.newscom.com/cgi-bin/prnh/20100315/SF69879-a)

(Logo: http://www.newscom.com/cgi-bin/prnh/20100315/SF69879LOGO-b)

"For decades, the Holy Grail for electronic system manufacturers has been a logic platform that combines programmabili and flexibility with large capacity and cost effectiveness. Semico believes that the unique approach to how Tabula accomplishes this is truly innovative and is something the entire semiconductor industry should take note of," said Richar Wawrzyniak, Sr. Market Analyst at Semico Research Corp. "Tabula's benchmark results for ABAX show significant advantages in both density and capability for logic, memory and signal processing when compared with 40 nm FPGAs, enabling ABAX devices to offer programmability both in traditional FPGA applications and beyond."

Deployed on TSMC's 40 nm process, ABAX devices integrate a rich mixture of fully configurable, high-performance I/Os, including 920 general-purpose parallel I/Os, and 48 6.5Gbps serial transceivers. To improve time to market and producti the ABAX family's design flow closely resembles those for FPGAs and ASICs, using synthesis, placement, and routing tc compile designs from RTL into silicon. In addition, ABAX devices support a broad portfolio of soft IP cores, including DDF and DDR3 memory controllers, PCI Express, Gigabit and 10 Gigabit Ethernet, soft CPUs, sRIO, CPRI, and OBSAI.

"We are proud to announce our 40 nm ABAX family of Spacetime 3PLD devices," said Dennis Segers, Tabula Chief Executive Officer. "We believe the combination of our breakthrough Spacetime architecture, our commitment to advance process technology nodes, and our focus on standard design flows set Tabula and ABAX apart in the market. With the support of a seasoned executive staff, a world-class engineering team, and a premier investor group, we look forward to demonstrating to customers the value ABAX can bring to their applications."

ABAX Product Information

The initial ABAX products to be released by Tabula will be the A1EC02, A1EC03, A1EC04, and the A1EC06. A list of the features is outlined below. For more information, please visit http://www.tabula.com/products/overview.php.

	ABAX Product Family			
Features	A1EC02	A1EC03	A1EC04	A1EC06
MegaLUTs	0.22	0.30	0.39	0.63
MegaBYTEs RAM	5.5	5.5	5.5	5.5
RegFile Blocks	960	960	960	960
LRAM Blocks	480	480	480	480
MRAM Blocks	240	240	240	240
Multiplier/Accumulator Blocks	-	-	-	1,280
Parallel I/Os	920	920	920	920
SerDes (55 Mb/s - 6.5 Gb/s)	48	48	48	48
PLLs	44	44	44	44

ABAX Pricing and Availability

ABAX A1EC04 samples will be available in Q3 2010, and will go into mass production in Q4 2010. Pricing in 2010 for the A1EC04 is $150 for orders of 2,000 units.

About Tabula

Tabula is a privately held fabless semiconductor company developing 3-D Programmable Logic Devices. Its ABAX family 3PLDs, based on Tabula's patented Spacetime architecture, sets new density, performance, and affordability benchmark programmable logic, memory, and signal processing. Headquartered in Santa Clara, California, Tabula has assembled a leadership team consisting of industry veterans and successful entrepreneurs. The company is backed by top-tier investo with a long-term view toward enduring market leadership. For more information, please visit the Tabula website at www.tabula.com.

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