



## Virage Logic Executives Slated to Speak at Key Industry Events

### ***Industry IP Experts to Deliver Insightful Perspectives for the Business, Technology, and Defense Communities***

FREMONT, Calif., Mar 06, 2009 (BUSINESS WIRE) -- Virage Logic Corporation (NASDAQ:VIRL), the semiconductor industry's trusted IP partner, announced today that some of the company's key executives have been invited to speak at upcoming industry events.

Dr. Alex Shubat, Virage Logic's president and CEO, will be a featured speaker at the 12<sup>th</sup> Annual Semico Summit being held at the Camelback Inn in Scottsdale, Arizona, on March 8-10, 2009. The [Semico Summit](#) will feature industry leaders addressing the outlook of the semiconductor industry, including Dr. Shubat speaking on "*Manufacturing: Managing Cost and Risk.*"

Virage Logic's Dr. Yervant Zorian, vice president and chief scientist, is known as one of the industry's leading authorities on Design for Manufacturing (DFM) and will be featured at the 10<sup>th</sup> International Symposium on Quality Electronic Design ([ISQED](#)) in a dinner panel discussion titled, "*DFM: Insurance Policy or Secret Weapon?*" taking place from 6:30-8:30 pm on March 17, 2009, at the DoubleTree Hotel in San Jose, California.

The upcoming Government Microcircuit Applications Critical Technology Conference ([GOMACTech 2009](#)), being held on March 16-19, 2009 at the DoubleTree Hotel Universal in Orlando, Florida, will feature Virage Logic's Joel Rosenberg, senior marketing director, presenting a technical paper on, "*Embedded Flash on a CMOS Logic Process Enables Security on Deep Submicron Designs.*"

### **About Virage Logic**

Virage Logic is a leading provider of semiconductor intellectual property (IP) for the design of complex integrated circuits. The company's highly differentiated product portfolio includes embedded SRAMs, embedded NVMs, embedded test and repair, logic libraries, memory development software, and interface IP solutions. As the industry's trusted semiconductor IP partner, foundries, IDMs and fabless customers rely on Virage Logic to achieve higher performance, lower power, higher density and optimal yield, as well as shorten time-to-market and time-to-volume. For further information, visit <http://www.viragelogic.com>.

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SOURCE: Virage Logic Corporation

Virage Logic Corporation  
Sabina Burns, 510-743-8115  
[sabina.burns@viragelogic.com](mailto:sabina.burns@viragelogic.com)

or  
McClenahan Bruer Communications  
Tarah Hartzler, 503-546-1014  
[tarah@mcbru.com](mailto:tarah@mcbru.com)

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