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IMEC Joins SOI Industry Consortium

IMEC has joined the SOI Industry Consortium, adding its SOI-related research interests to the platform development efforts of the consortium. "SOI has long been one of the key routes on IMEC's roadmaps," said IMEC COO Luc Van den Hove.

Staff -- Semiconductor International, 2/23/2009 7:29:00 AM

IMEC (Leuven, Belgium) has joined the [SOI Industry Consortium](#) (Boston) as an academic member, raising membership in the consortium to 26 organizations.

"SOI has long been one of the key routes on IMEC's roadmaps," said IMEC COO Luc Van den Hove. "In semiconductor technology, platform creation is of vital importance in making progress, creating critical mass, and identifying common interests between various players. That is what IMEC is doing, and that is what the SOI Industry Consortium is doing."

IMEC is processing both partially depleted (PD) and fully depleted (FD) SOI devices at its R&D fab in Belgium.

SOI Industry Consortium members include AMD, Applied Materials, ARM, Cadence Design Systems, CEA-Léti, Chartered Semiconductor Manufacturing, Freescale Semiconductor, IBM, IMEC, Innovative Silicon, KLA-Tencor, Lam Research, Magma Design, Nvidia, Samsung, Semico, Soitec, SEH Europe, STMicroelectronics, Symmid Semiconductor, Synopsys, Time To Market, TSMC, Tyndall Institute, University Catholique de Louvain and UMC.

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