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## SOI Consortium Formed With 19 Members

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A group of 19 companies and research centers today announced the **SOI Industry Consortium**, which they said is “aimed at accelerating silicon on insulator [SOI] innovation...and reducing the barriers to adoption.”

The founding members include Advanced Micro Devices (AMD), ARM, Cadence Design Systems, CEA-Leti, Chartered Semiconductor Manufacturing, Freescale Semiconductor, IBM, Innovative Silicon, KLA-Tencor, Lam Research, NXP, Samsung, Semico, Soitec, SEH Europe, STMicroelectronics, Synopsys, Taiwan Semiconductor Manufacturing Co. (TSMC) and UMC.

“Riding the performance wave, SOI has made significant inroads,” said André-Jacques Auberton-Hervé, the CEO of **Soitec** and the consortium’s chair. “Now the focus has expanded to reducing power consumption. SOI can cut power consumption significantly.”

The consortium will focus on ensuring that user needs are heard, accelerating collaboration to enable silicon-proven solutions, and promoting SOI’s benefits to the greater electronics community.

The consortium’s initial focus is on sharing best practices already established by early adopters and facilitating new design proof points demonstrating SOI’s performance, power and area advantages.

The board of directors will be elected later in October.

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