



Sandeep Vij

**President and Chief Executive Officer
MIPS Technologies**

Mr. Vij brings to MIPS Technologies more than 20 years of senior-level management and marketing experience in the semiconductor industry. Prior to joining MIPS, he was Vice President and General Manager of the Broadband and Consumer Division of Cavium Networks, a leading provider of highly-integrated semiconductor products that enable intelligent processing in networking, communications, and connected home applications.

Prior to Cavium, Mr. Vij was on the executive staff of Xilinx Inc. for 12 years, where he was instrumental in growing the company's business by 3X to over \$1.8 billion per year and enabling it to become the industry's largest programmable logic vendor. Mr. Vij also held roles at Xilinx as Vice President and General Manager of its General Products Division, and Vice President of Worldwide Marketing, Services and Support. Prior to Xilinx, Mr. Vij spent five years in various senior roles at Altera Corporation.

Mr. Vij is a graduate of General Electric's Edison Engineering Program and Advanced Courses in Engineering. He sits on the Board of Directors of Coherent Inc. He holds an MSEE from Stanford University and a BSEE from San Jose State University.