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Mark Papermaster is the Vice President of the Switching Silicon Technology Group (SSTG) leading a new organization that brings together the silicon design skills for Cisco's switching businesses. SSTG has responsibility for the ASIC strategy, architecture, and development for Catalyst and Nexus switching products. In addition, Papermaster is a member of the Silicon Technology board driving silicon technology investment direction across Cisco.

Prior to joining Cisco, Papermaster was senior vice president of Apple Devices Hardware Engineering responsible for iPod and iPhone. Prior to Apple, Papermaster held a number of leadership roles at IBM in ASIC, microprocessor, and system product development.

Papermaster received his bachelor's degree in electrical engineering from University of Texas at Austin, and a master's degree in electrical engineering from University of Vermont. He is a member of the University of Texas college of Engineering Advisory Board, and the Juvenile Diabetes Research Foundation IT Advisory Committee.