



Joe Sawicki

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Sawicki is the vice president and general manager of the Design-to-Silicon division. A leading expert in IC nanometer design and manufacturing challenges, Sawicki is responsible for Mentor's industry-leading design-to-silicon products, including the Olympus-SoC place-and-route system, the Calibre physical verification and DFM platform, and Mentor's design-for-test product line. Sawicki joined Mentor Graphics in 1990 and has held previous positions in applications engineering, sales, marketing and management. He holds a BSEE from the University of Rochester and an MBA from Northeastern University's High Technology Program.